

Électronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

56633

Application ID:

10619638

Content Addressable Memory

(CAM) Devices Having Error

Title of Invention:

Detection and Correction Control

Circuits Therein and Methods of

Operating Same

First Named Inventor:

Chuen-Der Lien

Domestic/Foreign Application:

Domestic Application

Filing Date:

2003-07-15

Effective Receipt Date:

2004-03-05

Submission Type:

Information Disclosure

Statement

Filing Type:

Confirmation number:

1228

Attorney Docket Number:

5646-91IP

Total Fees Authorized:

Digital Certificate Holder: cn=Grant J. Scott,ou=Registered Attorneys,ou=Patent and Trademark

Office,ou=Department of Commerce,o=U.S. Government,c=US

Certificate Message Digest: d0ef33296ef330ba0c7826dd1a7bd2b99d3d23c5



TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention Content Addressable Memory (CAM) Devices Having Error Detection and Correction Control Circuits Therein and Methods of Operating Same

Application Number:

10/619638

Date:

2003-07-15

First Named Applicant:

Chuen-Der

Confirmation Number:

1228

Attorney Docket Number: 5646-91IP

I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Submitted by:	Elec. Sign.	Sign. Capacity
Grant J. Scott Registered Number: 36925	/gjs/	Attorney

Documents being submitted Files

us-ids

5646-91IP-2-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Content Addressable Memory (CAM) Devices Having Error **Detection and Correction Control Circuits Therein and** Methods of Operating Same

Application Number:

10/619638

Confirmation Number:

1228

First Named Applicant:

Chuen-

Attorney Docket Number: 5646-91IP

Art Unit:

2818

Search string:

(6597595).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init,	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6597595	2003-07-22	Ichiriu et al.	B1	365	49

Signature

Examiner Name	Date		
·			